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Computer Organization

**Computer Organization and
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CS-224 Computer Organization

2.01 Instruction Code GTU -
**Computer Engineering (CE) - Semester
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**Computer Organization and Design
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1.01 Register Transfer Language
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2.04 Control Unit of Basic Computer
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***The MIPS Processor (Computer
Organization)*** - **معماری پردازنده MIPS | معماری
پردازنده MIPS mips processor architecture**
(Arabic) **Computer Organization**
chapter **4** (the Processor) part 2 :
<https://youtu.be/3a-xlgzwdOk> ...

***Computer Organization and
Architecture(COA)***

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instruction codes in computer architecture | COA

Instruction Codes i.e memory reference, register reference and I/O reference immediate, direct and indirect address.

Computer Organization and Design-6: Instructions Sets and their Operands

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ة و م ج م ة ي م ه ا ح ر ش ل و ا ن ت ي و ي ج ه ن م ل ا
ة... ه ت ف ي ظ و و (instruction set) ت ا ز ا ع ي ا ل ا

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